Fig.1

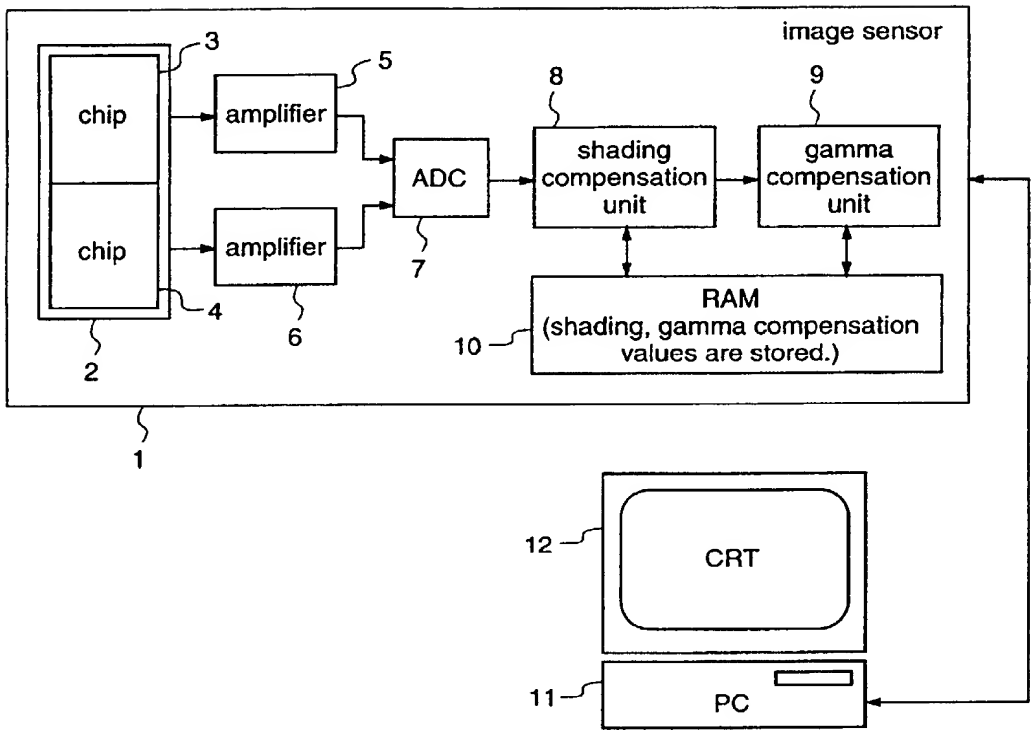


Fig.2 (a)

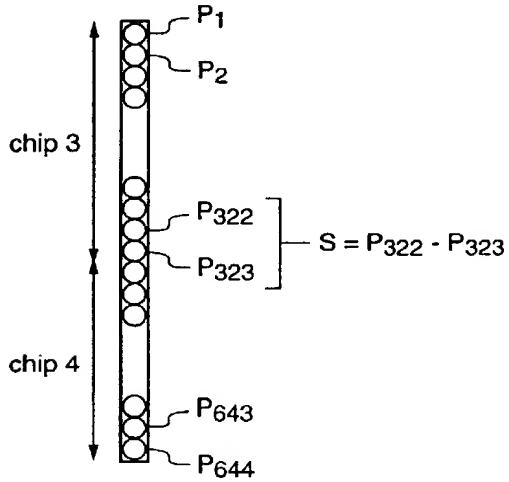


Fig.2 (b)

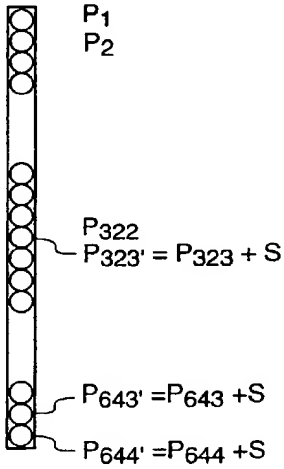


Fig.3 (a)

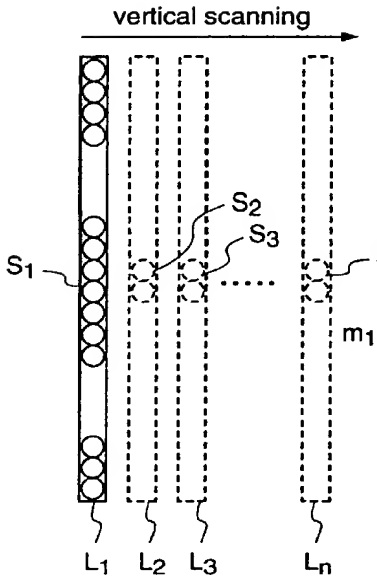
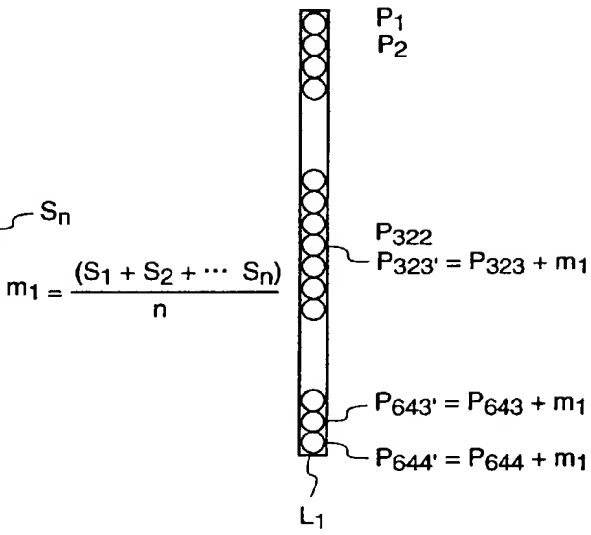


Fig.3 (b)



$$m_1 = \frac{(S_1 + S_2 + \cdots S_n)}{n}$$

Fig.3 (c)

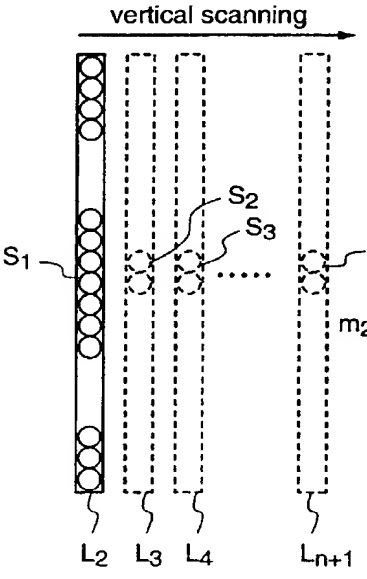
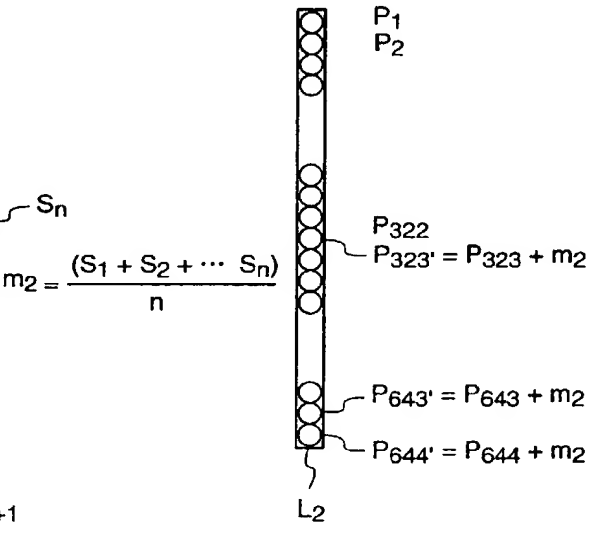


Fig.3 (d)



$$m_2 = \frac{(S_1 + S_2 + \cdots S_n)}{n}$$

Fig.4

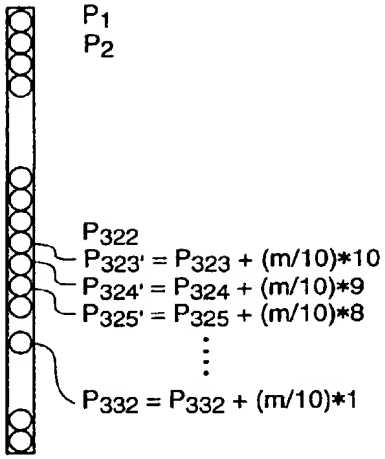

P1
P2
P322
P323' = P323 + (m/10)*10
P324' = P324 + (m/10)*9
P325' = P325 + (m/10)*8
P332 = P332 + (m/10)*1

Fig.5

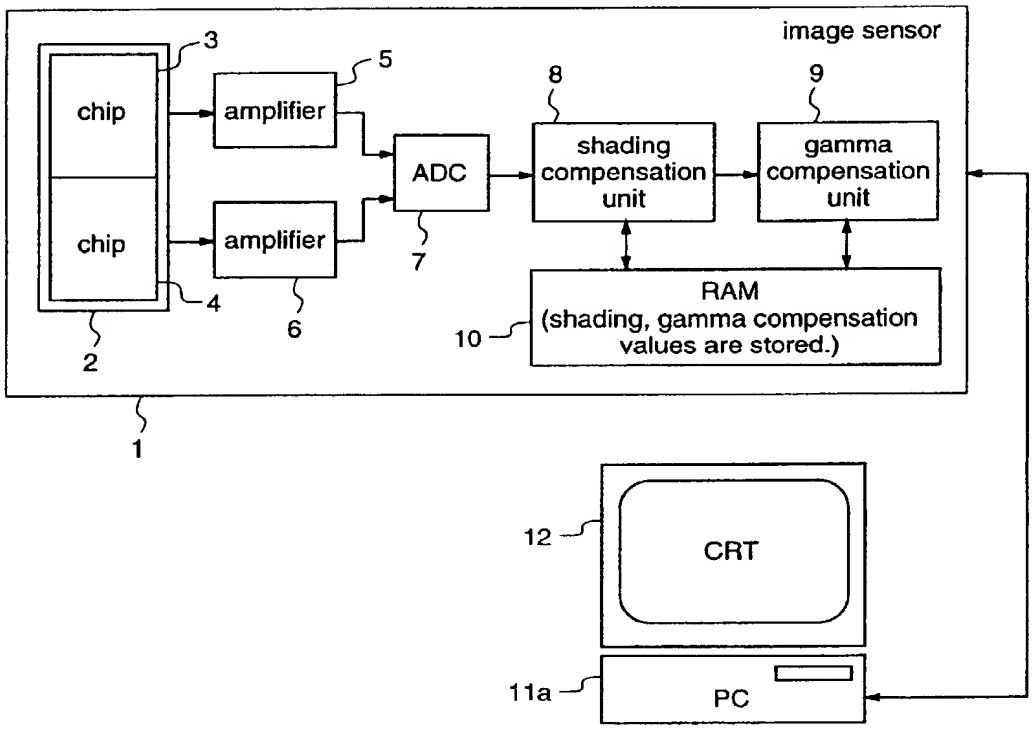


image sensor
chip
chip
amplifier
amplifier
ADC
shading compensation unit
gamma compensation unit
RAM
(shading, gamma compensation values are stored.)
1
2
3
4
5
6
7
8
9
10
12
CRT
11a
PC